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U.S. UTILITY Patent Application

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**APPLICANTS: Katagiri Souichi; Yamaguchi Ui; Kondo Seiichi; Yasui Kan; Kawamura Yoshio;					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-127043 04/25/2001 YJS AR 12/17/02					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO NITT.0063	
Verified and Acknowledged Examiners's initials		AR 12/17/02			
TITLE : Method and apparatus for manufacturing semiconductor device					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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